IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To AP 5 Reco PCT/PTO 28 MAR 2005

March 28, 2006

In Re PATENT APPLICATION Of:

: Kazumasa TANIDA et al. Applicant : To Be Assigned U.S. Appln. No. International Appln. No.: PCT/ JP05/010549 INFORMATION International Filing Date: June 9, 2005 DISCLOSURE **STATEMENT** Priority Date Claimed : June 16, 2004 : SEMICONDUCTOR DEVICE For

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

: AI 406NP

Sir:

Attorney Ref.

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(2).

Attached are copies of eleven Japanese patents, any relevance of which to the claimed invention can be gleaned from the attached English-language Abstracts, the attached English-language international search report, computer translations, the U.S. counterpart patents, and/or from pages 1 and 5 of the specification, where some of the documents are discussed. All of the publications are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 are three U.S. patents and six U.S. Patent Application Publications for consideration.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all documents listed. Consideration of the listed documents is respectfully requested.

Please charge any further fee to our Deposit Account FEE ENCLOSED:\$ No. 18-0002 March 28, 2006 Date

Respectfully submitted,

Robert H. Berdo, Jr. (Reg. No. 38,075)

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RHB:tl

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT			Docket No.: Al 406NP	International No.: PCT/JP04/010549			
			Applicant: Kazumasa TANIDA et al.				
					-	Group Art Unit: To Be Assigned	
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